

Docket No.: 246575US6



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION: Takahito NAKANO, et al.

SERIAL NO.: 10/735,680

GAU: 2181

FILED: December 16, 2003

EXAMINER: FRANKLIN, R.

FOR: SEMICONDUCTOR INTEGRATED CIRCUIT APPARATUS AND CIRCUIT BOARD
AND INFORMATION READOUT METHOD

LETTER SUBMITTING REPLACEMENT DRAWING SHEET(S)

COMMISSIONER FOR PATENTS
Alexandria, VA 22313

SIR:

Responsive to the below indicated communication, the following drawing sheets are submitted herewith:

☒ 1 Replacement Drawing Sheets ☐ _____ New Drawing Sheets

☒ Official Action dated March 3, 2006

☐ Notice of Allowance/Issue Fee dated _____

☐ Other dated _____

The changes and/or modifications made include the following:

Figure 5 of said drawings has been designated as "Prior Art" and labeled as "Replacement Sheet" in the page header pursuant to Office Action dated March 3, 2006.

Respectfully Submitted,

OBLON, SPIVAK, McCLELLAND,
MAIER & NEUSTADT, P.C.

Bradley D. Lytle
Registration No. 40,073

Customer Number

22850

Tel. (703) 413-3000
Fax. (703) 413-2220
(OSMMN 06/04)

Kurt M. Berger, Ph.D.
Registration No. 51,461

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